

Fig. 1

T080T"8E6800T

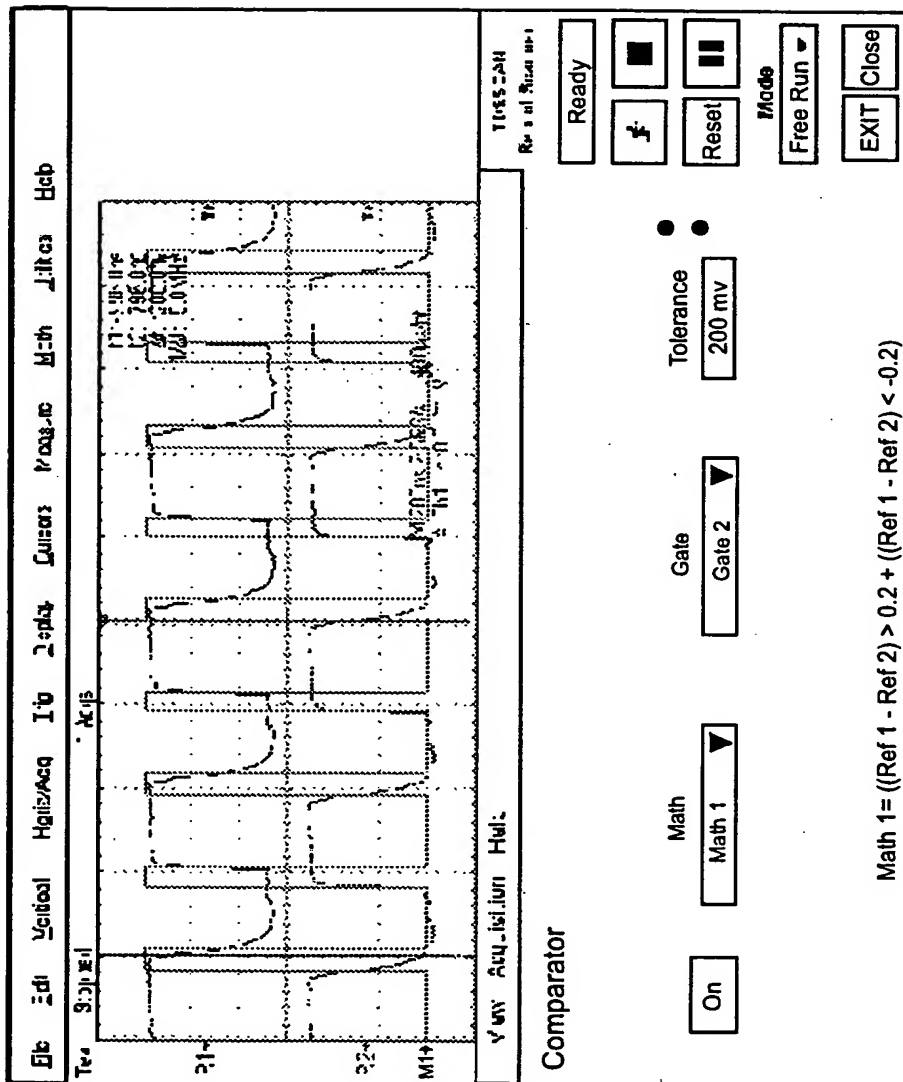


Fig. 2

10008938-110801

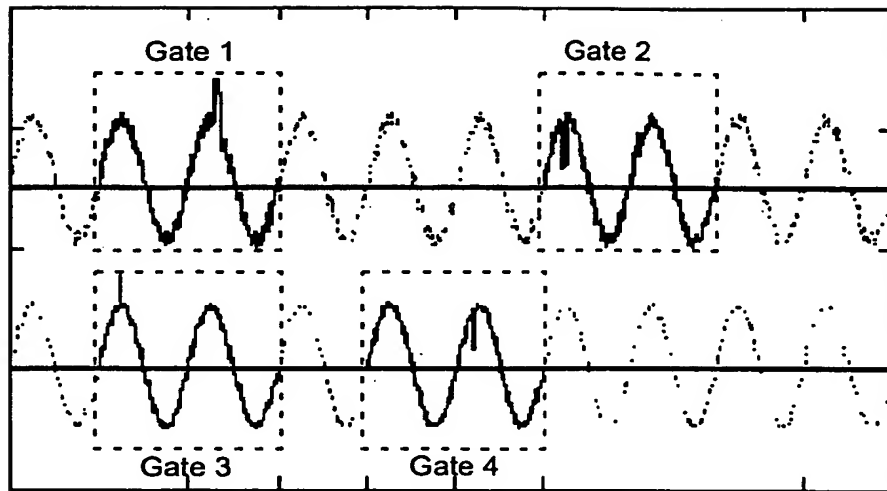


Fig. 3

Fig. 4A

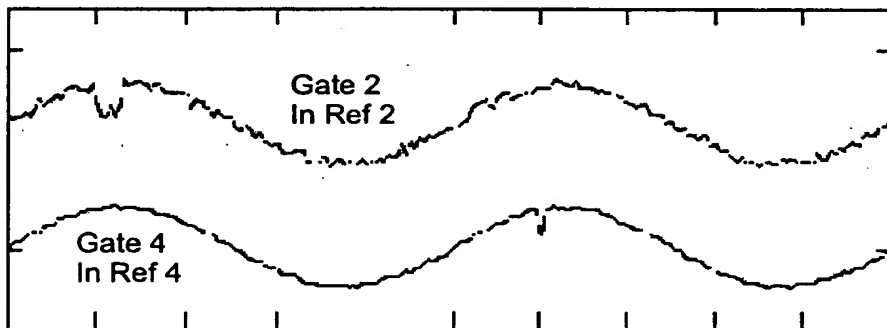
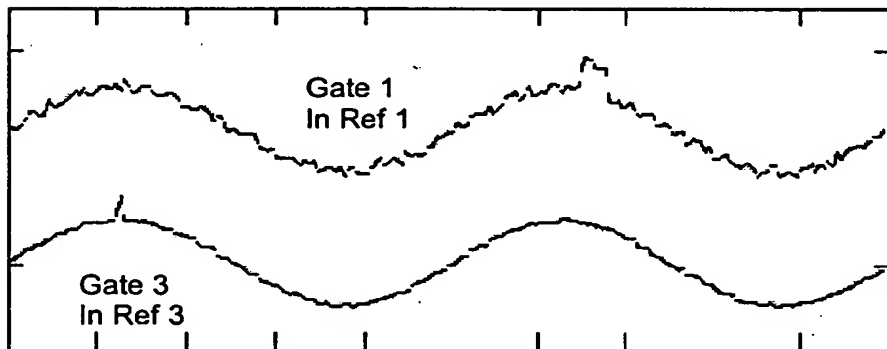


Fig. 4B

T080TT"8E68000T

Gate Comparator			
Master	Position	Width	Comparator
<input type="checkbox"/> Close <input checked="" type="checkbox"/> On	<input type="text" value="50"/>	<input type="text" value="50"/>	<input type="text" value="On"/>
Tolerance <input type="text" value="50"/>			
Step Size	Gate 1		
<input type="text" value="1"/>	Position <input type="text" value="50"/>		
<input checked="" type="checkbox"/>	Source <input type="text" value="Ch 1 ▼"/>		
<input type="checkbox"/>	<input type="text" value="On"/>		
Gate 2			
Position <input type="text" value="50"/>			
Source <input type="text" value="Ch 2 ▼"/>			
<input type="text" value="On"/>			
Gate 3			
Position <input type="text" value="50"/>			
Source <input type="text" value="Ch 1 ▼"/>			
<input type="text" value="Off"/>			
Gate 4			
Position <input type="text" value="50"/>			
Source <input type="text" value="Ch 2 ▼"/>			
<input type="text" value="Off"/>			

Fig. 5

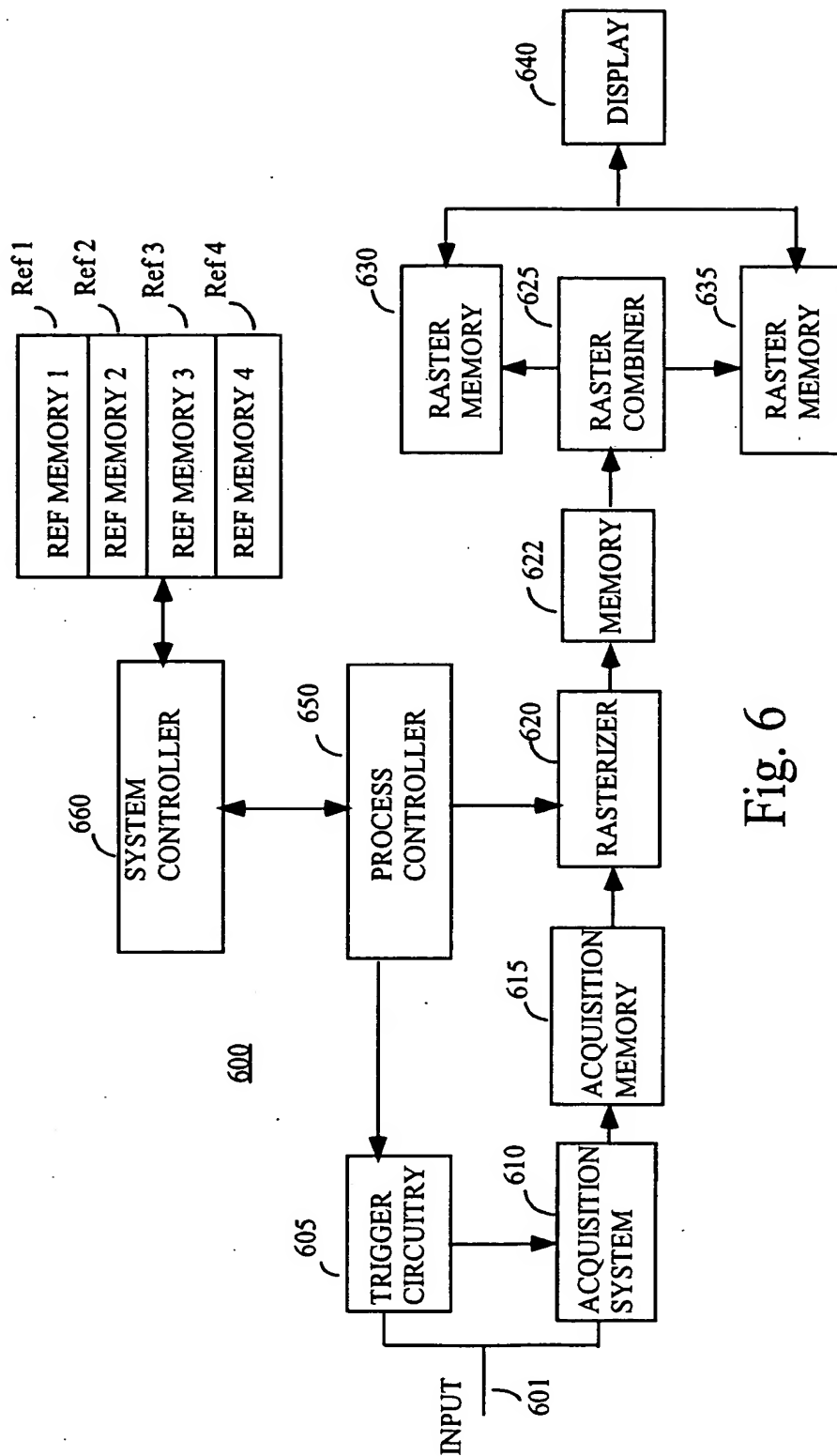


Fig. 6